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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/656,576	09/05/2003	Cheon Hong Kim	CU-3351 RJS	7892
26530	7590	03/28/2006	EXAMINER	
LADAS & PARRY LLP			WU, XIAO MIN	
224 SOUTH MICHIGAN AVENUE			ART UNIT	PAPER NUMBER
SUITE 1600				2629
CHICAGO, IL 60604				

DATE MAILED: 03/28/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	10/656,576	KIM ET AL.	
	Examiner XIAO M. WU	Art Unit 2674	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 05 September 2005.  
 2a) This action is FINAL.                            2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-4 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1 and 2 is/are rejected.  
 7) Claim(s) 3 and 4 is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____. 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6) <input type="checkbox"/> Other: _____.
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**DETAILED ACTION**

***Claim Rejections - 35 USC § 103***

1. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

2. Claims 1 and 2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ikeda (US Patent No. 4,393,405) in view of Ozawa et al. (US Patent No. 6,839,045)

As to claim 1, Ikeda discloses a liquid crystal display (Fig. 1) comprising: thin film transistors (102) connected to intersections between a plurality of data lines (D1...Dn) and a plurality of gate lines (G1...Gm); pixel electrodes (e.g., top portion electrode of the LCD cell 104), each of which is connected to a source of each of the thin film transistors; common electrodes (e.g. bottom portion electrode of the LCD cell 104) opposed to the pixel electrodes; liquid crystal (104) injected between the pixel electrodes and the common electrodes. It is noted that Ikeda does not disclose a plurality of auxiliary gate lines corresponding to the gate lines, and first capacitors, each of which is connected between the source and each of the auxiliary gate lines.

Ozawa is cited to teach an active matrix flat panel display similar to Ikeda. Ozawa further discloses a plurality of auxiliary gate lines (e.g. cline line as shown in Fig. 2)

corresponding to the gate lines (gate line shown in Fig. 2), and first capacitors (e.g. cap as shown in Fig. 2), each of which is connected between the source and each of the auxiliary gate lines (see Fig. 2). It would have been obvious to one of ordinary skill in the art to have modified Ikeda with the features of the auxiliary gate lines as taught by Ozawa because the capacitor connected to the auxiliary gate line can maintain the pixel voltage even the transistor is turned “off” (see col. 9, lines 50-67).

As to claim 2, Ikeda as modified discloses a second capacitor 103) is connected between the source and each of the common electrodes (e.g. the capacitor 102 is connected to the common electrode of the pixel in the same common line as shown in Fig. 2).

***Allowable Subject Matter***

3. Claims 3 and 4 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

The US 4,870,396, 4,928,095, 5,568,163, 5,923,310, 5,952,991, 6,064,360, and 2004/0012554 are cited to teach a liquid crystal display device.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to XIAO M. WU whose telephone number is 571-272-7761. The examiner can normally be reached on 6:30 am to 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RICHARD HJERPE, can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

X.W.

March 19, 2006



**XIAO M. WU**  
**Primary Examiner**  
**Art Unit 2629**